VIAVI

Xgig[®] 1000 NVMe and PCI Express 3.0 Protocol Analyzer



The Xgig 1000 analyzer for NVMe and PCI Express 3.0 is the latest addition to the Viavi Solutions family of high-speed, serial protocol-analysis solutions.

It seamlessly connects with the existing Xgig family of SAS, SATA, Ethernet, FC, and FCoE analyzers, providing an integrated trace view of all high-speed traffic in today's complex, multi-protocol environments.

This powerful tool also triggers and captures all SSD-based NVMe, PCle, and AHCI-SATA Express traffic. Triggers include speed change, MSI+MSI-X, and all NVMe standard and custom OP codes. The analyzer easily captures and displays NVMe-specific features like PRP tables and dynamic queue updates. And, the 64 G buffer (32 G x 2) is lane-width independent, so full buffer captures are supported for all X1, X2, X4, and X8 traffic.

The analyzer ships with a standard X8 PCIe Gen3 interposer, and a dedicated Viavi team works with each customer on solving physical analyzer/system connection challenges. SFF 8639, M.2, and low-profile server interposer solutions are also available. Viavi also has an array of 3rd-party cable and board solutions that solve many of today's connection challenges. Please contact your local Viavi representative for details.

Specifications

Hardware	
Data rates supported	2.5 GT/s, 5.0 GT/s, and 8 GT/s
Lanes width	x1, x2, x4, and x8
Trace buffer	64 GB (32 GB upstream and 32 GB downstream)
Trace memory divided per port	
Memory segmentation for capture of multiple traces	
Communication interfaces	Gigabit Ethernet, USB 3.0
Dimensions	14 x 9 x 3.5 in; 35 x 22 x 10 cm
Weight	13.5 lb, 6.2 kg
Power	100 – 240 VAC; 500 W max



Contact Us +1 844 GO VIAVI (+1 844 468 4284)

To reach the Viavi office nearest you, visit viavisolutions.com/contacts.

Key Features

- Supports native PCIe and PCIe Gen3 at all link speeds
- Supports NVMe with full trigger capability on messages
- Decodes all PCIe/NVMe traffic including TLP, DLLP, training sequences, ordered sets, queue pairs, PRPs, and configuration space
- Displays packets on a time-synchronized trace; protocols can be toggled on/off for ease of debug
- Supports SRIS
- Decodes all NVMe and PCIe data to text no more referring to the spec
- One-year hardware and three-year software warranties
- Cascadable with other Viavi analyzers to display multi-protocol packets on a single trace
- Portable, lightweight, and stand-alone





© 2015 Viavi Solutions, Inc. Product specifications and descriptions in this document are subject to change without notice. xgig1000-pcieg3-analyzer-ds-snt-nse-ae 30175824 900 0714